



SV SensTech
— 华景传感科技 —

Data Sheet

Version 1.0/ NOV 2021

ML-2718-B1Z1

拥有核心芯片技术的MEMS传感技术公司

A MEMS Sensor Company with Advanced Core Chip Technology



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无锡

研发测试中心：无锡高新区



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华北销售中心：北京海淀



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深圳

华南销售中心：深圳南山



Product Specification

ML-2718-B1Z1 Bottom -port analog silicon microphone



● Description

ML-2718-B1Z1 is miniature, an analog bottom-port silicon microphone that receives the sound signal from the backside hole on PCB. By using our own innovative and unique MEMS silicon microphone chip design, MicroLink Senstech provides a series of packaged MEMS silicon microphones with compact size to achieve good performance such as high SNR, high sensitivity, excellent reliability and broad frequency response. This series of MEMS microphones find wide applications in cell phones, tablets, wearable devices and other portable electronic devices.

● Features

- Compact size of $2.75 \times 1.85 \times 0.9 \text{ mm}^3$
- Sensitivity of -38 dBV/Pa ($\pm 1 \text{ dBV/Pa}$)
- High signal-to-noise ratio of 66 dB
- Extremely low THD of 0.1% at 94 dB SPL
- High acoustic overload point of 128 dB SPL
- High immune to RF/EM interference
- High mechanical strength
- High temperature resistance
- Excellent reliability

● Acoustic and electrical specifications

Test conditions: 23°C±2°C, 55%±5% R.H., VDD=1.8V, no load, unless otherwise indicated

Parameter	Symbol	Condition	Min.	Typ.	Max	Unit
Sensitivity	Sens	94dB SPL @1kHz	-39	-38	-37	dBV/Pa
Signal to Noise Ratio	SNR	20Hz to 20KHz, A-weighted	-	66	-	dB (A)
		20Hz to 7KHz, A-weighted	-	68	-	dB (A)
Output Impedance	Z _{out}	@ 1 kHz	-	-	300	Ω
Supply Voltage	V _{DD}		1.6	-	3.6	V
Current Consumption	I _{DD}		-	125	160	μA
Dimension			2.75×1.85×0.9			mm ²
Directivity			Omnidirectional			
Frequency Response	F		20~16k			Hz
Power Supply Rejection	PSR	217Hz, 0.1Vpp Square on VDD	-	-105	-	dBV (A)
Power Supply Rejection Ratio	PSRR	200mVpp sine wave @ 1 kHz	-	70	-	dB
Total Harmonic Distortion	THD	94 dB SPL @ 1kHz	-	0.1	-	%
		114 dB SPL @ 1kHz	-	0.5	-	%
Acoustic Overload Point	AOP	1% THD @ 1kHz		117		dB SPL
		10% THD @ 1kHz	-	128	-	dB SPL

● Frequency response

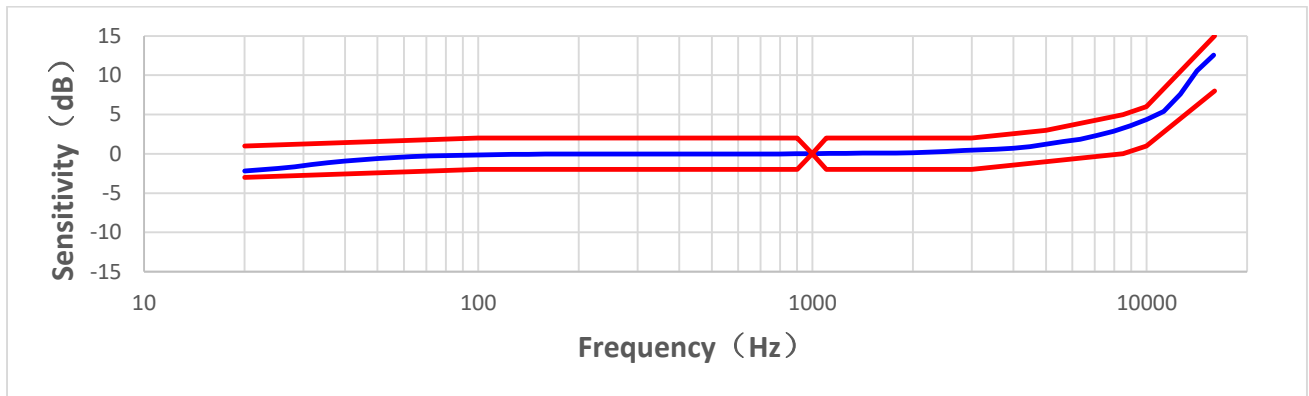
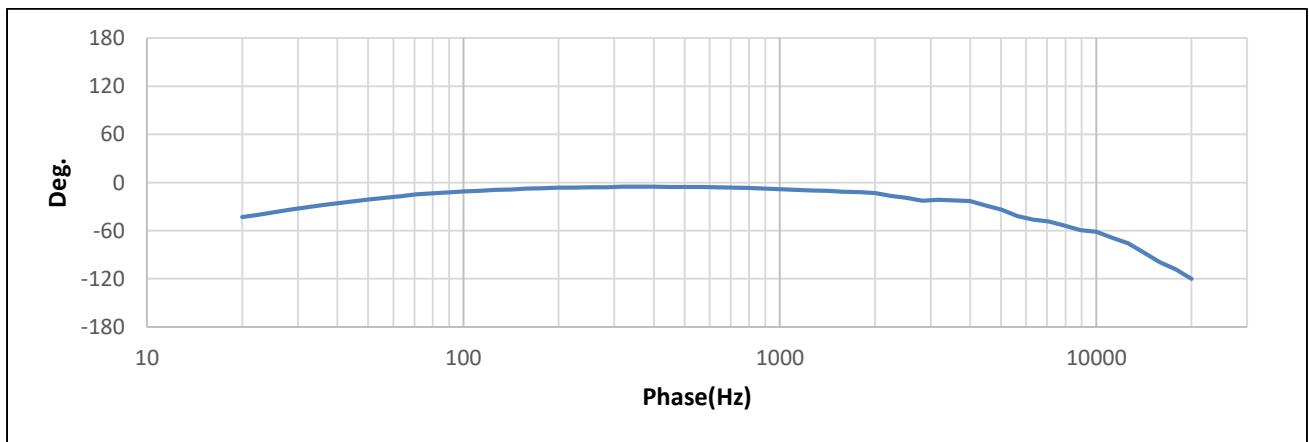
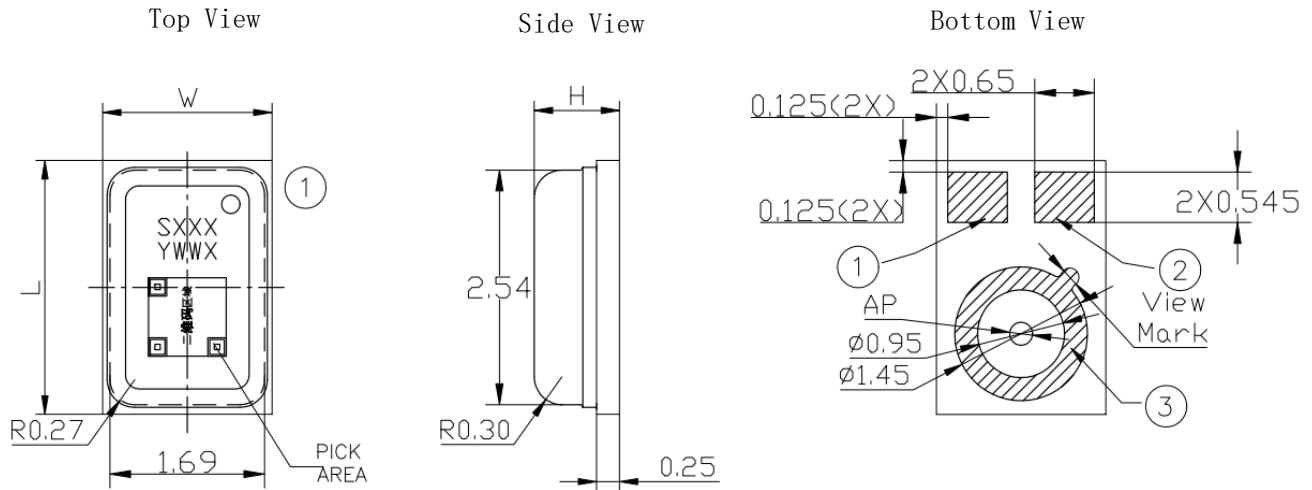


Figure 1. Typical free field frequency response (Normalized to 1 KHz)

● Phase Frequency response



● Mechanical specifications

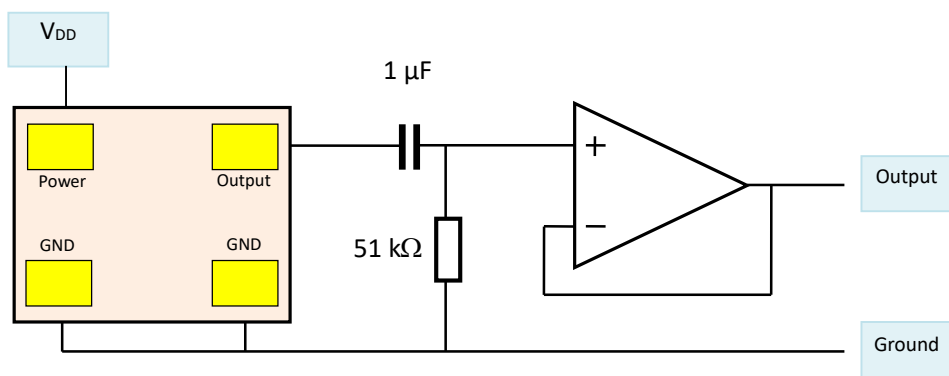


Dimension		
Item	Dimension	Tolerance
Length(L)	2.75 mm	±0.1 mm
Width(W)	1.85 mm	±0.1 mm
Height(H)	0.9 mm	±0.1 mm
AP	0.25 mm	±0.05 mm

Pin #	Pin Name	Description
①	VDD	Vdd
②	OUT	Signal output
③	GND	Ground

Figure 2. Detailed mechanical drawings

● Application circuit diaphragm



● Example land pattern

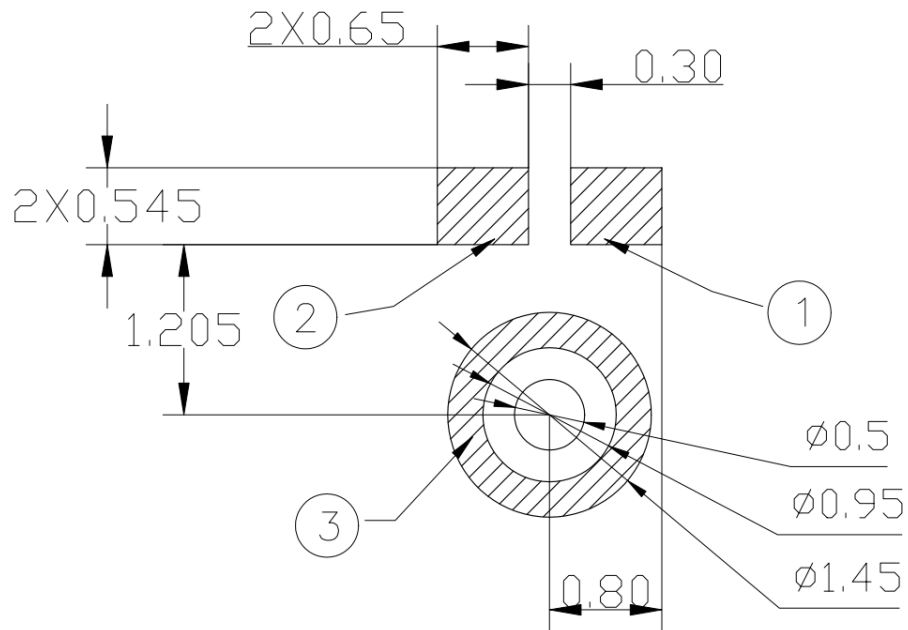
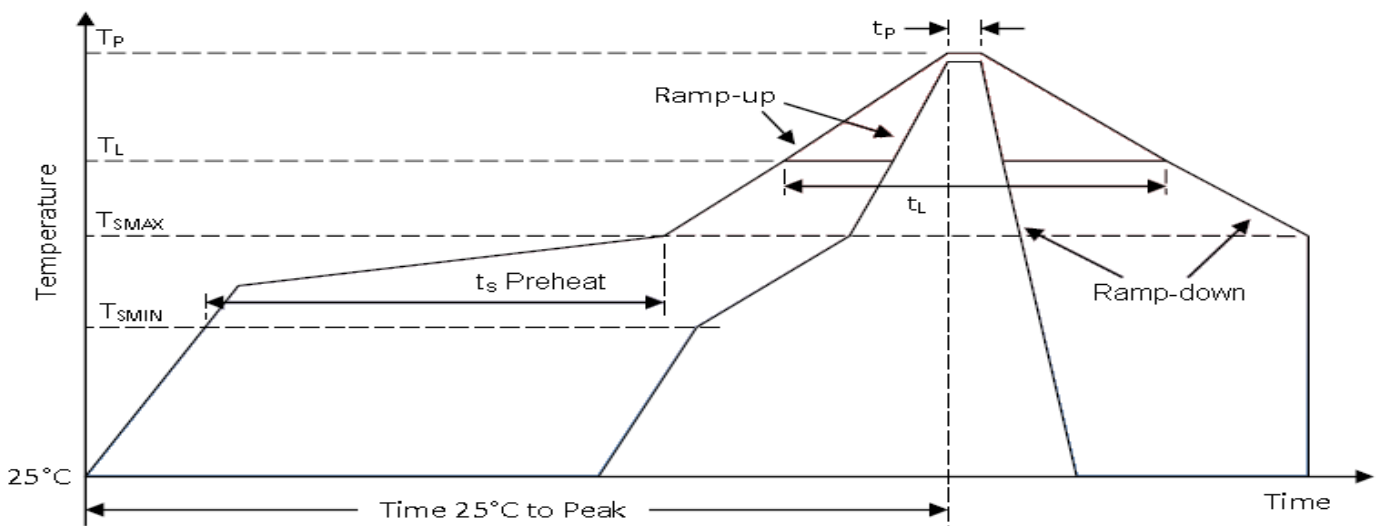


Figure 3. Recommended landing pattern on customers' PCB

● Recommended reflow profile



Description	Parameter (lead-free)
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Average temperature change rate (T_{MAX} to T_{P})		3°C/second max.
Preheat	Minimum temperature (T_{MIN})	150°C
	Maximum temperature (T_{MAX})	200°C
	Time (T_{MIN} to T_{MAX}) (t_{S})	60-180 seconds
Reflow	Temperature (T_{L})	217°C
	Time (t_{L})	60-150 seconds
Peak temperature	Temperature (T_{P})	260°C
	Time (t_{P})	20-40 seconds
Cooling rate (T_{P} to T_{MAX})		6°C/second max
Time required from 25°C to peak temperature		8 minutes max

Figure 4. Recommended leadless solder reflow temperature profile

Notes:

- 1) The air blow speed during reflow process should be low to avoid impurity entering the acoustic hole during reflow process.
- 2) Do not wash or clean the product to avoid impurity entering the product.
- 3) Do not carry out the reflow process more than 5 times. If the melting point of solders is lower, the peak temperature should be accordingly reduced.

● Reliability specifications

Test Item	Description
Temperature/Humidity Bias	1,000 hours at +85°C/85% R.H. under bias (JESD22-A101A-B)
Thermal shock	100 cycles air-to-air thermal shock from -40°C to +125°C with 15minute soaks. (IEC 68-2-4)
High Temperature Storage	1,000 hours at +105°C environment (IEC 68-2-2)
Low Temperature Storage	1,000 hours at -40°C environment (IEC 68-2-1)
High Temperature Bias	1,000 hours at +105°C under bias (IEC 68-2-2)
Low Temperature Bias	1,000 hours at -40°C under bias (IEC 68-2-1)
Drop test	Using 150g fixture, 3 drops along each of 6 axes from 1.5m height onto slippery marble floor (IEC 68-2-27)
ESD-HBM	3 discharges of ± 3 kV direct contact to I/O pins. (MIL 883E, Method 3015.7)
ESD-LID/GND	3 discharges of ± 8 kV direct contact to lid while unit is grounded. (IEC 61000-4-2)
ESD-MM	3 discharges of ± 200 V direct contact to I/O pins. (ESD STM5.2)
Vibration test	4 cycles of 20 to 2,000 Hz sinusoidal sweep with 20 G peak acceleration lasting 12 minutes in X, Y, and Z

- Meets the requirements of the industry standard IEC 61249-2-21:2003 for halogenated substances and SV SensTech Green Materials Standards Policy section on Halogen-Free.

● Remarks

- MSL (moisture sensitivity level) Class 1.
- Maximum of 3 reflow cycles is recommended.
- In order to minimize device damage:
- Do not board wash or clean after the reflow process.
 - Do not brush board with or without solvents after the reflow process.
 - Do not directly expose to ultrasonic processing, welding, or cleaning.
 - Do not insert any object in the port hole of device at any time.
 - Do not apply over 30 psi of air pressure into the port hole.
 - Do not pull a vacuum over the port hole of microphone.
 - Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.

● Version updates

Version	Updated Content	Updated Date
1.0	Initial release	2021-11-30

● Contact

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