



SV-DIP6-010D



- ~
- μ
- 2
- ~



3.

-
-
-
-

4.

1

		-40	+105	°C
		-0.3	+3.6	V
IO		-0.3	+3.6	V
ESD		-2	+2	kV
			30	kPa



5.

2

Vs=3.3VDC TA=25°C

	TA			-20	25	85	°C	
				0		65	°C	
	P			0		10	kPa	
	VDD			1.8	3.3	3.6	V	
1 Hz	Idd		OSR_P	OSR_T			μA	
			1024X	2048X		10		
			2048X			14		
			4096X			19		
			8192X			28		
			16384X			46		
	Iddsbm	@25			0.1			
/	Tc		OSR_P	OSR_T			ms	
			1024X	2048X		13		
			2048X			19		
			4096X			31		
			8192X			56		
			16384X			105		
ADC					24		Bit	
					16		Bit	
	P_A	0-65°C			1		%F.S	
	T_A	0~65°C			1		°C	
	fi ² C	I ² C				3.4	MHz	



6.

2

2

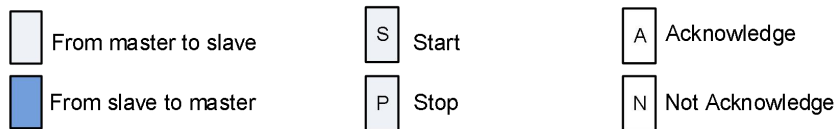
6.1

I²C

7bits I²C

0x78

6.2 I²C



0xF0

7bits I²C

0x78

1bit

0

6.3 I²C

0xF1

7bits I²C

0x78

1bit

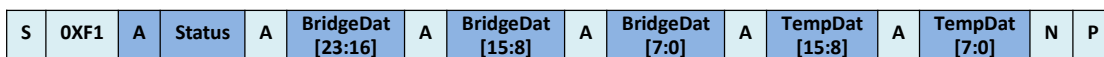
1

6

1

3

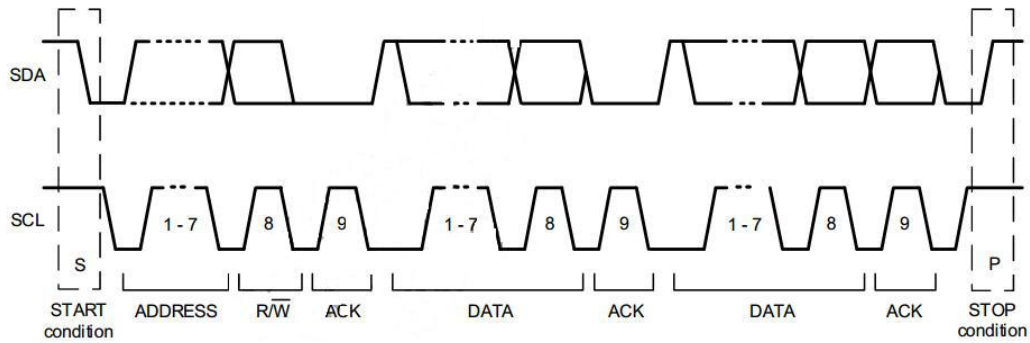
2



1

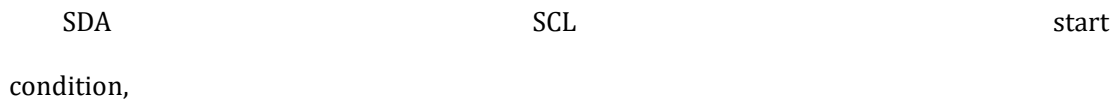


6.4 I²C



2 I2C

- START Condition



- Address Bits



- Read/Write Direction Bit



- Data Byte



- Acknowledge or Not Acknowledge Bit



SDA SDA
SDA

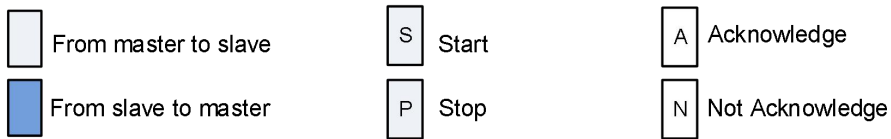
➤ Stop Condition

SDA SCL I²C

7.

0xAC 0xAC

7.1





0xF0 7bits I²C 0x78 1bit 0

7.2

[15:14] OTP (Address: 0x14) [13:11] OTP(Address: 0x14)

IIC

4 0xF1 7bits I²C 0x78 1bit 1

6 1 3 2

S	0XF1	A	Status	A	BridgeDat [23:16]	A	BridgeDat [15:8]	A	BridgeDat [7:0]	A	TempDat [15:8]	A	TempDat [7:0]	N	P
---	------	---	--------	---	-------------------	---	------------------	---	-----------------	---	----------------	---	---------------	---	---

4 I²C 5

7.4

0x04 0x9B 0xB0 0xC5 0x56 0xAA

0x04 Bit5 1 I²C Bit5 0

0x9B 0xB0 0xC5

0x56 0xAA

0x9B 0xB0 0xC5

10203333,

0Kpa-10Kpa

AD



2516582.4~14260633.6 15%AD~85%AD

P2

$$= 10-0 / 14260633.6-2516582.4 * 10203333-2516582.4 +0 = 6.545 \text{ KPa}$$

0x56 0xAA

22186

16 bits

65535

$$22186/65536 * 100\% = 33.85\%$$

-40 ~150

$$=(150-(-40)) * 33.85\% - 40 = 24.32$$

:

$$Pressure = \frac{P_{MAX} - P_{MIN}}{D_{MAX} - D_{MIN}} \cdot (D_{test} - D_{MIN}) + P_{MIN}$$

Pressure

Dtest

P_{MIN}

P_{MAX}

D_{MIN}

D_{MAX}



4 Status

Bit7		0
Bit6	(Power indication)	1 V _{DDB} on 0
Bit5	(Busy indication)	1 I ² C 0 I ² C
Bit4		0
Bit[3]	(Mode Status)	0 NOR mode 1 CMD mode
Bit2	(Memory integrity/error flag)	0 OTP (CRC) 1 (POR) CRC POR
Bit1		0
Bit0		0

7.5 Get_Cal_S

Get_Cal_S (0xB9 ~ 0xBE) Get_Cal (0xAC) ADC

Get_Cal OTP

Get_Cal_S OTP

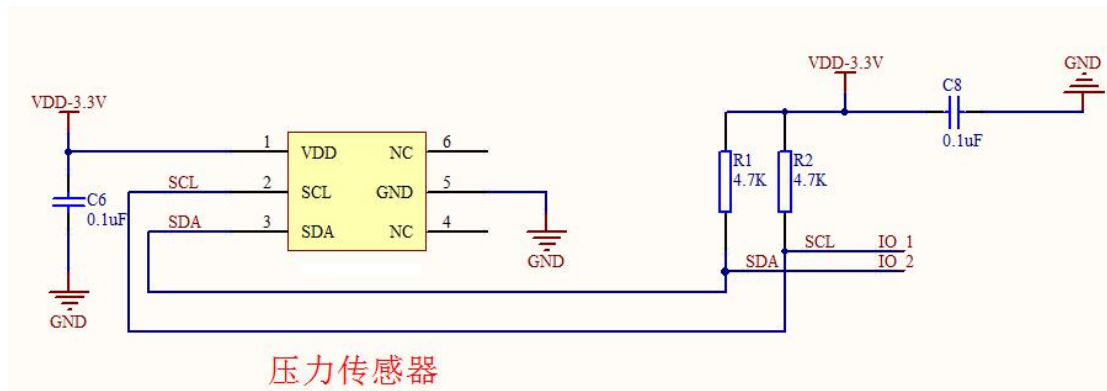
Get_Cal_S



5 Get_Cal_S

Command 0xBX(HEX)		Function	Detail
X	[3] Bit	ADC OSR_T	1: 8x
X	[2:0] Bit	ADC OSR_P	001: 16384x 100: 2048x 010: 8192x 101: 1024x 011: 4096x 110: 512x

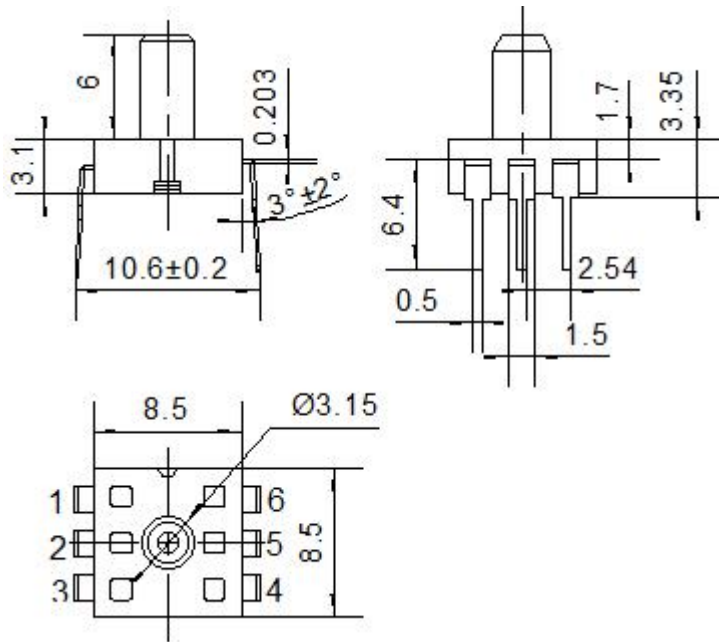
8.





9.

9.1



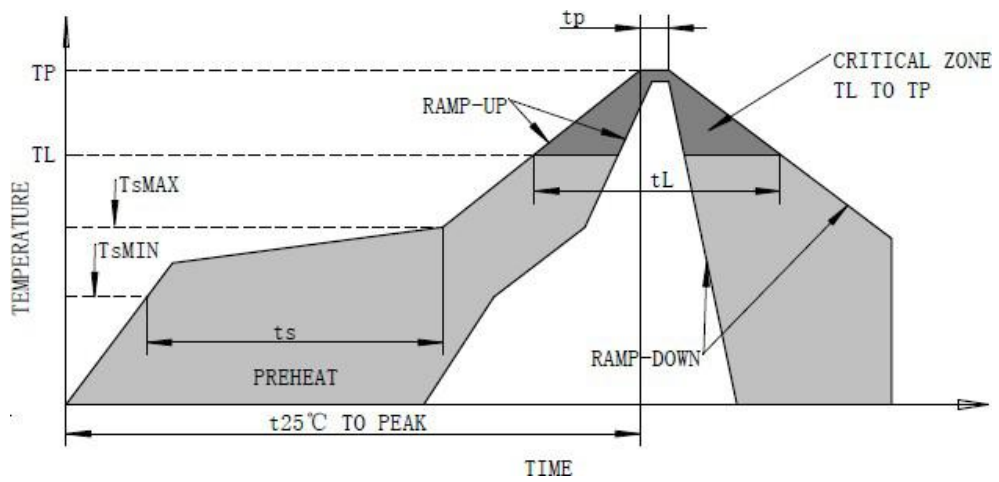
6

6

		2
		2



10.



		3 /
-	TsMIN	150
-	Tsmax	200
-	TsMIN TsMAX Ts	60 80
-	TL	217
-	tL	60 150
	TP	260
	TP 5 tP	20 40
		4 /
25		8



11.

		2020-6-18

12.

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